

2822

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Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Kim et al.)
ı) Examiner: Michael M. Trinh
Application No.: 10/077,967) Art Unit: 2822
Filing Date: February 20, 2002 For: PROCESS OF VERTICALLY STACKING MULTIPLE WAFERS	i hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Commissioner for Patents, PO Box 1450, Alexandria, Virginia 22313-1450
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The individual whose signature appears below is authorized to execute this Power of Attorney on behalf of Intel Corporation.

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